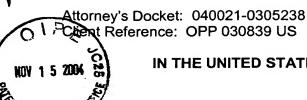
The



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION of:

Confirmation Number: 9322

PARK

Application No.: 10/626,549

Group Art Unit: 2814

Filed: July 25, 2003

Examiner: H. PHAM

For: METHOD FOR MANUFACTURING A MOS TRANSISTOR THAT PREVENTS CONTACT SPIKING AND SEMICONDUCTOR DEVICE EMPLOYING A MOS TRANSISTOR MADE USING THE SAME (AS AMENDED)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

AMENDMENT/RESPONSE TRANSMITTAL

Transmitted herewith is an amendment/response for this application.

FEES

The fee for claims and extension of time (37 C.F.R. 1.16 and 1.17) has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NO. PREVIOUSLY PAID FOR		PRESENT EXTRA		RATE			ADDIT. FEE		
TOTAL	6		20	=	0	x	\$	18.00	_=_	\$	0.00	
INDEP.	1_		3	=	0	X	\$_	88.00	=	\$	0.00	
FIRST PRESENTATION OF MULTIPLE DEP. CLAIM + \$ 300.00									=	\$	0.00	
TOTAL ADDITIONAL CLAIM FEE										\$	0.00	
GRAND TOTAL										\$	0.00	

FEE PAYMENT

Authorization is hereby made to charge the amount of \$0.00 to Deposit Account No. 033975. Charge any additional fees required by this paper or credit any overpayment in the manner authorized above.

Date: November 15, 2004
PILLSBURY WINTHROP LLP
P.O. Box 10500

McLean, VA 22102 703.905.2088 E. RICO HERNANDEZ

Reg. No. 47641



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Title: METHOD FOR MANUFACTURING A MOS TRANSISTOR THAT PREVENTS

CONTACT SPIKING AND SEMICONDUCTOR DEVICE EMPLOYING A MOS

TRANSISTOR MADE USING THE SAME (AS AMENDED)

November 15, 2004

AMENDMENT UNDER 37 C.F.R. § 1.111

Mail Stop Non-Fee Amendments Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated October 29, 2004, please amend the above-identified application as follows:

PARK -- 10/626,549

Client/Matter: 040021-0305238

IN THE TITLE:

Please delete the present title and replace it with the following new title:

-- METHOD FOR MANUFACTURING A MOS TRANSISTOR THAT PREVENTS CONTACT SPIKING AND SEMICONDUCTOR DEVICE EMPLOYING A MOS TRANSISTOR MADE USING THE SAME --.